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Digital Single Market

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ECSEL is a partnership between the private and the public sectors for electronic components and systems.



[ECSEL](#) [2] is operating since 2014 and will run for 10 years, replacing the previous [ENIAC](#) [3] and [ARTEMIS](#) [4] in the fields of nanoelectronics and embedded systems.

Goals

- maintain semiconductor and smart systems manufacturing capability in Europe and help it grow
- secure a leading position in design and systems engineering
- provide access for all stakeholders to a world-class infrastructure for the design and manufacture of electronic components and embedded and smart systems
- foster the development of ecosystems involving innovative SMEs, strengthen (and create) clusters in promising new areas

Benefits for Europe

At a larger scale, ECSEL will:

- Secure supply of key technologies supporting innovation in all major sectors of the economy In this way Europe assures its independence in the field of electronic components and systems
- Align strategies with Member States to attract private investment and avoid duplication of efforts. So, Europe does not pay twice for the same work or resources, done in different member states
- Push industry to set a long-term strategic research and innovation agenda
- Support EU policies, as embodied in the [Europe 2020 strategy](#) [5] for growth
- Overcome the obstacles to effective research and innovation in this area

Partners

The partners in the ECSEL Joint Undertaking are:

- the European Union (through the Commission);
- Member States and Associated Countries to the [Framework Programme Horizon 2020](#) [6] on a voluntary basis;
- 3 private industrial associations ([EPoSS](#) [7], [AENEAS](#) [8] and [ARTEMISIA](#) [9]) representing the actors from the areas of micro-/nanoelectronics, smart integrated systems and embedded/cyber-physical systems

More information:

- [Memo](#) [10]
- [Press release](#) [11]
- [Fact Sheet](#) [12]
- Micro-/nanoelectronics: [ENIAC](#) [3] and [AENEAS](#) [8]
- Embedded/cyber-physical systems: [ARTEMIS](#) [13] and [ARTEMIS Joint Undertaking](#) [4]
- [Smart integrated systems](#) [14]
- [European Industrial Strategic Roadmap for Micro- and Nano-Electronic Components and Systems](#) [15]

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21 March 2018

Team responsible

[DG CONNECT](#) [16]

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[2] <http://www.ecsel-ju.eu/>

[3] <http://www.eniac.eu/>

[4] <http://www.artemis-ju.eu/>

[5] http://ec.europa.eu/europe2020/index_en.htm

[6] <http://ec.europa.eu/research/horizon2020>

[7] <http://www.smart-systems-integration.org/public>

[8] <http://www.aeneas-office.eu/>

[9] <http://www.artemis-ia.eu/contact>

[10] <https://ec.europa.eu/digital-single-market/node/67178>

[11] <https://ec.europa.eu/digital-single-market/node/67180>

[12] http://ec.europa.eu/research/press/jti/factsheet_ecsel-web.pdf

[13] <http://www.artemis-ia.eu/>

[14] <http://www.smart-systems-integration.org/>

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<https://ec.europa.eu/digital-single-market/en/news/european-industrial-strategic-roadmap-micro-and-nano-electronic-components-and-systems>

[16] https://ec.europa.eu/info/departments/communications-networks-content-and-technology_en