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## Digital Single Market

Report / Study 21 March 2013

# Future impact of ENIAC and ARTEMIS

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The study is an impact assessment of a new JTI resulting from the merging of the 2 existing JTIs (ENIAC and ARTEMIS) and the corresponding strategic research agenda (SRA), as well as the possible inclusion of the microsystems activities of the EPoSS ETP. It provides a support in the preparation of the Commission's proposal on the setting up of this potential JTI and its governance and financial rules to be implemented.

Contact

[1]

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