

PROPOSERS'DAY

26-27 September 2012 - Warsaw

Objective ICT-2013.3.1: Nanoelectronics

Draft agenda of the networking session 27 September, 9:00 – 10:30, Room 2 "Components", Hall 1

9:00	Overview of Objective 3.1 – Nanoelectronics by Isabel Vergara, European Commission, Unit A4 – Components
9:15	Presentations by proposers
	- Compact models and circuit simulators based on CMG MOSFETs - Spyridon NIKOLAIDIS (Aristotle University of Thessaloninki, Physics, Greece)
	- Non-Destructive Mapping of Warpage in Si Die Embedded in Packaged Integrated Circuits. Nick Bennett, Dublin City University
	- Dr Dariusz Litwin. Optical Measurement Technology. Local Contact Point for Research Programmes of the EU
10:15	Meet the speaker – (15 min time to contact speakers at the "meet-the-speaker" corner directly outside the room)
10:30	End of the session

If you are interested in presenting your proposal idea at the event, you can submit a short presentation until 24 September through the website. European Commission staff will inform you whether your presentation will be added to the event programme shortly before the event.

Invited speakers will be given a ~5 minute timeslot. All presentations, selected and non-selected, will be published on the event website.

The remaining part of the event is dedicated to networking among the participants. From the side of the European Commission, Isabel Vergara, and Silvia Andreescu will be present to answer all your questions about Objective 3.1.

For further information:

Components



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European Commission - Directorate-General for Communications Networks, Content & Technology

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