



Professor Igor RADUSINOVIC

Igor Radusinovic, born 1972, received the Diploma Degree from the Faculty of Electrical Engineering, University of Montenegro, Montenegro, in 1994 and the MSc and PhD Degrees from the University of Belgrade, Serbia, in 1997 and 2003, respectively. Doctoral thesis title “Analysis of ATM switch from angle of queue location and management”.

Professor Radusinovic was appointed to his current position of Deputy Minister, Department for Science, Research and Technology Development, Ministry of Education and Science, Government of Montenegro in 2009. He is a responsible for research, research infrastructure and international research cooperation. He has been the Montenegrin representative to the Board of Governors of the Joint Research Centre of the European Commission since 2009.

Professor Radusinovic currently holds the position of associated professor at the Faculty of Electrical Engineering, University of Montenegro. He is a member of the Engineering Chamber of Montenegro.

Professor Radusinovic joined the Faculty of Electrical Engineering, University of Montenegro in 1994. He has supervised 4 MSc theses and 60 diploma dissertations. He has served as reviewer for leading scientific journals in the different areas of telecommunications, as well as member of Programme Committees of several Conferences.

His research interests are mainly in telecommunications network protocols and systems design. His current research topics are: packet switch architectures, quality of service in wired/wireless networks and congestion control in wireless networks. In these areas he has published more than 100 referred publications in peer-review international journals and international proceedings.